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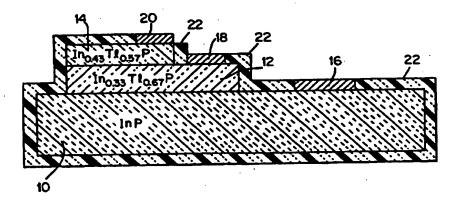
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(54) Title: COMPOUNDS AND INFRARED DEVICES INCLUDING $In_{1-x}TI_xQ$, WHERE Q IS $As_{1-y}P_y$ AND $0 \le y \le 1$



(57) Abstract

A semiconductor layer of $In_{1-x}TI_xQ$ carried on a substrate forms an infrared device, where Q is selected from the group consisting essentially of $As_{1-y}P_y$ and 0 < x < 1, $0 \le y \le 1$.

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COMPOUNDS AND INFRARED DEVICES INCLUDING $In_{1-x}T\ell_xQ$, WHERE Q IS $As_{1-y}P_y$ AND $0 \le y \le 1$

Field of Invention

The present invention relates generally to compounds including indium and thallium and infrared devices including same, and more particularly to compounds of $In_{1-x}T\ell_xQ$, where Q is selected from the group consisting essentially of $As_{1-y}P_y$, 0< x<1, and $0< y\le 1$, and to infrared detector and emitter devices including same.

Background Art

The prior art reports staring, i.e., non-scanned, infrared focal plane detector arrays formed of mercury cadmium telluride (Hg,Cd,Te), indium antimonide (InSb) and Pt:Si for midwave infrared (3-5 microns) and mercury cadmium telluride (Hg.78Cd.22Te) for long wavelength (8-12 microns) purposes. An enormous effort has brought these technologies to a mature state where arrays as large as 512 by 512 pixels are manufactured with practical yields, at high, but marginally acceptable, cost. One major failing of the mercury cadmium telluride and indium antimonide based staring focal plane detector arrays is that individual elements of the array must be indium bump-bonded to a readout integrated circuit on silicon substrates as disclosed, for example, in Timlin et al. U.S. Patents 5,227,656 and 5,304,500. Such bump-bonding techniques limit array sizes and stability. numbers of native defects also limit performance in

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mercury cadmium telluride arrays. While the platinum silicon arrays can be potentially integrated onto the same silicon chip that houses the readout integrated circuit, such arrays have low quantum efficiencies of approximately 1%, to severely limit performance.

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As further demands are made for increased signal-tonoise ratios at higher, or even, non-cryogenic operating temperatures, multi-spectral responses, large arrays of small pixels, longer operational lifetimes and lower production costs, it is dubious if currently available materials will be satisfactory. In the past, several other materials have been suggested to meet these These other materials have been built of requirements. strained layer superlattice structures and have been formed as quantum well infrared photoconductors. other materials can be built on gallium arsenide substrates, enabling them to be fabricated using monolithic integration with a readout integrated circuit on the same chip. However, these prior art devices have low quantum efficiencies, less than 10%, and quantum wells having awkward optical arrangements.

It is, accordingly, an object of the present invention to provide a new and improved infrared device.

Another object of the present invention is to provide a new and improved infrared detector that can be grown on a substrate that also carries a read-out integrated circuit, to obviate the requirement for bump-bonding.

Another object of the invention is to provide a new and improved infrared detector that can be grown on an indium phosphide substrate and is substantially lattice matched to the indium phosphide, to obviate the need for superlattice structures.

Another object of the invention is to provide a new and improved infrared detector compound that can be tailored, with the selection of appropriate mole

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fractions, to detecting differing wavelengths in the infrared spectrum.

Another object of the invention is to provide a new and improved pseudo-binary alloy compound, particularly adapted for use in infrared detectors and emitters.

Summary of the Invention

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In accordance with one aspect of the present an infrared detector or emitter invention, comprises a substrate and a semiconductor layer $In_{1-x}T\ell_xQ$ carried by the substrate, where Q is selected from the group consisting essentially of As_{1-v}P_v and 0 < x < 1, $0 \le y \le 1$. In first and second embodiments of the invention, y=1 and y=0, respectively, so that the layer consists essentially of semiconductor In, TlP In__xTlAs. Based on investigations we have conducted, semiconductor layers of $In_{1-x}T\ell_xP$ and $In_{1-x}T\ell As$ are rugged and can be epitaxially grown in zinc blend structure for all values of x with lattice constants nearly matching those of InP and InAs, respectively. Such epitaxially grown semiconductor layers of $In_{1-x}T\ell_xP$ and $In_{1-x}T\ell As$ are comparatively free of native point defects and have the high mobility and infrared absorption characteristics needed for high performance infrared detector and emitter devices. Based on our investigations, TlP is a semimetal having a negative band gap (-0.27eV) analogous to that of mercury telluride (-0.30eV). The band semiconductor $In_{1-x}T\ell_xP$ (the band gaps of semiconductors positive) spans the entire midwavelength infrared spectra, for different values of x less than 0.76.

InP is desirable for the substrate of $In_{1-x}T\ell_xP$ because it is a high mobility, low dislocation electronic material similar to gallium arsenide, capable of forming ohmic contacts as well as p and n hetero- and homojunctions. In addition, InP has a functional

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passivant/insulator (SiO2), enabling it to support high performance read-out integrated circuit devices. based on our investigations, semiconductor $In_{1-x}T\ell_xP$ epitaxially grown on indium phosphide (InP) can satisfy all system requirements. Semiconductor $In_{1-x}T\ell_xAs$ is also a suitable infrared detector with many of the same properties of $In_{1-x}T\ell_xP$. InAs is the substrate best suited for $In_{1-x}T\ell_xAs$. However, $In_{1-x}T\ell_xP$ is preferred over In, T/As because InP is a better substrate than InAs. This is because InP can be doped, has better device performance and can be lattice-matched better semiconductor $In_{1-x}T\ell_xP$ than semiconductor $In_{1-x}T\ell_xAs$ can be matched to InAs. In addition, semiconductor $In_{1-x}T\ell_xP$ and substrates offer the advantage of integration into a readout integrated circuit chip.

We are aware that attempts have been made to produce long wavelength infrared detectors in which layers of indium thallium antimony compounds $(In_{1-x}T\ell_xSb)$ deposited on indium antimonide (InSb). Applied Physics Letters, Vol. 62, page 1857 (1993) (M. von Schilfgaarde et al.). A significant drawback in the use of $In_{1-x}T\ell_xSb$ is that it favors a more closely packed structure to the zinc blend. However, the greatest disadvantage in the attempted use of $In_{1-x}Tl_xSb$, which does not appear to arise in $In_{1-x}T\ell_xP$ or $In_{1-x}T\ell As$, is that $In_{1-x}T\ell_xSb$ cannot be successfully grown on a zincblende lattice. This failure appears to occur because $In_{1-x}T\ell_xSb$ does not have a strong enough thermodynamic force to drive Tl onto the zincblende lattice.

The above and still further objects, features and advantages of the present invention will become apparent upon consideration of the following detailed descriptions of specific embodiments thereof, especially when taken in conjunction with the accompanying drawings.

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Brief Description of Drawing

Fig. 1 is a side view of a preferred embodiment of the invention;

Fig. 2 are plots of energy band gap vs. values of x for each of $In_{1-x}T\ell_xP$, $In_{1-x}T\ell_As$ and $Hg_xCd_{1-x}Te$; and

Fig. 3 are plots of electron mobility vs. temperature for each of $In_{0.33}T\ell_{0.67}P$, $In_{0.85}T\ell_{0.15}As$ and $Hg_{0.78}Cd_{0.22}Te$.

Description of the Preferred Embodiment

Reference is now made to Fig. 1 of the drawing wherein there is illustrated an infrared device capable of detecting long wavelength infrared radiation having a cutoff wavelength of 12 microns and for separately detecting medium wavelength infrared energy having a cutoff wavelength of 5 microns. The structure illustrated in Fig. 1 can be operated as a photovoltaic or as a photoconductive detector, depending on the bias voltages to which electrodes thereof are connected.

In the embodiment of Fig. 1, indium phosphide (InP), semi-metallic bulk substrate 10 has deposited thereon, preferably by any of liquid phase epitaxy, metalorganic chemical vapor epitaxy, metalorganic molecular beam epitaxy or molecular beam epitaxy methods, a rugged semiconductor alloy layer of n type indium thallium phosphide 12 $(In_{1-x_1}T\ell_{x_1}P)$ having a zincblende structure. InP substrate 10 is a good electronic material, i.e. has high carrier mobility, low dislocation density (similar to GaAs), has virtually no native point defects, easily has ohmic contacts and p-n junctions formed thereon, is easily doped, can easily be coated with a function passivant/insulator (SiO₂), and electronic devices formed thereon have good, consistent performance The value of x for the compound of characteristics. layer 12 is selected such that layer 12 absorbs long wavelength infrared energy preferably having a cutoff

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wavelength of 12 microns and is approximately lattice matched to InP substrate 10. Based on studies we have performed, lattice matching is about 1% and the 12 micron wavelength cutoff are attained with a value of $x_1=0.67$, whereby layer 12 has a bandgap of about 0.1eV. Layer 12 is doped with silicon to achieve n type conductivity.

Deposited on layer 12, also by any of the foregoing methods, is further indium thallium phosphide a semiconductor layer 14 $(In_{1-x2}T\ell_{x2}P)$. Based on studies we have conducted, with x₂=0.57 rugged layer 14 absorbs midrange infrared energy having a cutoff wavelength of 5 microns (associated with a bandgap of 0.28eV), while passing the long wavelength infrared energy that is absorbed by layer 12. The indium thallium phosphide compound of layer 14 is doped with any one of zinc, magnesium or beryllium to form a p type layer, whereby a p-n homojunction is formed at the intersection of layers 12 and 14.

Aluminum ohmic contacts 16, 18 and 20 are respectively formed on exposed upper surfaces of layers 12 and 14 and substrate 10. Electrodes 16, 18 and 20 are connected to suitable electronic circuits to bias the device into a photoconductive state or enable the device to operate in the photovoltaic mode. All remaining, exposed surfaces of substrate 10 and of layers 12 and 14 are covered with passivating silicon dioxide (SiO₂) layer 20.

The structure of Fig. 1 can be modified to detect infrared energy having a single cut-off wavelength of 5 microns or 12 microns. To provide a cut-off wavelength of only 5 microns, layer 14 and electrode 16 associated therewith are eliminated and the device is arranged so the infrared energy is incident on layer 12. To provide a cut-off wavelength of 12 microns, layer 12 is replaced with a superlattice arrangement of $In_{1-x3}T\ell_{x3}P$ (where x_3

WO 96/05621

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varies from 0.67 to 0.57) that is lattice matched to substrate 10.

In an actual staring infrared focal plane detector, many devices of the type illustrated in Fig. 1 are arranged in a matrix of rows and columns on InP substrate 10 on which are also deposited CMOS bias and readout transistors, as well as metal row and column strips and other components.

While the preferred configuration includes an Inp substrate and one or more $In_{1-x}T\ell_xP$ layers, the substrate can also be bulk semimetallic carrying InAs semiconductor zincblende layer of In1-x3Tlx3As, where $0 \le x_1 \le 1$; for a 5 micron cut-off of the $In_{1-x_3}T\ell_{x_3}As$ layer, The invention is not limited to pseudobinary compounds $In_{1-x}T\ell_{x1}P$ and $In_{1-x2}T\ell_{x2}As$ for layers 12 and 14, but can be expanded to include the generalized pseudotertiary compound $In_{1-x_4}T\ell_{x_4}Q$, where Q is selected from the group consisting essentially of $As_{1-y}P_y$, where $0<x_4<1$ and 0<y<1. For y=0 or y=1, we have the specialized cases of the pseudobinary compounds $In_{1-x_3}T\ell_{x_3}As$ and $In_{1-x_3}T\ell_{x_3}P$, respectively. For 0<y<1, we have the above-noted generalized pseudotertiary compound which our studies show can detect and emit infrared energy to tailored wavelengths in the spectra of interest. It is to be understood that the substrates are not limited to the preferred compounds of InP and InAs but that the substrate can be formed of other materials, particularly silicon or gallium arsenide in semiconductor The $In_{1-x}T\ell_xQ$ layer carried by such a substrate is physically connected to the substrate by an appropriate superlattice structure.

Based on the studies we have performed, the bandgap energies (hence the cut-off wavelengths) of $In_{1-x}T\ell_xP$ and $In_{1-x}T\ell_xAs$ (shown in Fig. 2 by plotted lines 22 and 24, respectively) in accordance with this invention and the prior art compound $Hg_xCd_{1-x}Te$ (shown by plotted line 26)

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are illustrated as a function of the value of x in the interval 0≤x≤1. From Fig. 2, any desired band gap, hence cut-off wavelength, can be attained by proper selection of the value of x. The higher electron mobilities of the compounds of the present invention relative to the mobility of the prior art HgCdTe at temperatures approaching room temperature are clearly shown in Fig. 3. Fig. 3 includes plots based on our studies of electron mobility (in 105cm²/V.sec) vs. temperature of In_{0.33}Tℓ_{.67}P (line 28), In_{.85}Tℓ_{.13}As (line 30) and Hg_{.78}Cd_{.22}Te (line 32), all p doped with zinc at 10¹⁴/cm³ to have a band gap energy of 0.1eV. Hence, our studies show that layers of the present invention do not require the extensive refrigeration structure required by the prior art.

The ruggedness, i.e. structural stability, of the zincblende (four-fold coordination) $In_{1-x}T\ell_xP$ and $In_{1-x}T\ell_xAs$ lattice structures of the present invention can be determined from the binding energy of the atoms of these Our studies have shown these compounds in compounds. zincblende form to be light open structures having strong directional bonds relative to other compounds of the cations Al Ga and In with the anions P, As and Sb in more closely packed NaCl (six-fold coordination) and CsCl (eight-fold coordination) structures. For TlSb, the NaCl and $CsC\ell$ structures overtake the zincblende structure, a manifestation of which is a very negative band gap of This reversal in the ordering of the $T\ell Sb$ energy causes complications when attempts are made to grow the prior art $In_{1-x}T\ell_xSb$ alloy. Our studies show that $T\ell P$ and Tlas are stable relative to the more closely packed phases of the prior art compounds, whereby In1.xTlxP and $In_{1-x}T\ell_xAs$ are stable and can be produced without excessive problems.

Our studies show $In_{.33}Tl_{.67}P$ has excellent long wavelength infrared properties relative to the prior art Hq $_{.78}Cd$ $_{.27}Te$ because inter alia:

- 1. its 5.96 Å lattice constant closely matches the 5.83 Å lattice constant of the InP substrate on which it is deposited so the $In_{.33}T\ell_{.67}P$ liquidus and solidus phase diagrams have simple lens shapes;
- 2. the cohesive energy per atom (2.56eV/atom) of $T\ell P$ is 58% greater than that of HgTe (1.62eV/atom);

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- 3. TPP is a semimetal having a band gap of -0.27eV, about the same as HgTe (-0.3eV);
- 4. its band gap concentration variation (dEg/dx) of 1.42 is 16% smaller than Hg_{.78}Cd_{.22}Te;
- 5. its elastic constants are about 33% greater than those of Hg.78Cd.22Te;
- 6. the temperature variation of the band gap (dEg/dT) near 77°K is small (about -0.05 meV/°K), approximately 15% that of Hg.78Cd.22Te (about -0.36meV/°K); because of the low value of dEg/dT for In.33Tl.67P, design of circuits including that compound for variable temperature operation is greatly simplified and spatial variations in pixel performance of detector elements in a large matrix array due to temperature gradients within the array are virtually eliminated;
- 7. its electron effective mass is 0.008, equal virtually to that of Hg.78Cd.22Te; and
- 8. its hole effective mass is 0.37, 43% smaller than the 0.65 hole effective mass of Hg_{.78}Cd_{.22}Te, resulting in considerably higher hole mobility and substantially longer electron Auger recombination lifetimes for In_{.33}Tℓ_{.67}P.

While there have been described and illustrated several specific embodiments of the invention, it will be clear that variations in the details of the embodiments specifically illustrated and described may be made without departing from the true spirit and scope of the invention as defined in the appended claims. For example, the invention can be used to form a solar cell having a cut-off wavelength such that a very large

portion of the infrared spectrum is converted by photovoltaic action into electrical energy; in such an instance a layer of $In_{.24}T\ell_{.76}P$ is formed on an InP substrate. The compounds of the invention can also be used to form layers of infrared emitters in combination with the usual other structures of such emitters.

CLAIMS

- 1. An infrared detector or emitter device comprising a substrate, and a semiconductor layer of $In_{1-x}T\ell_xQ$ carried by the substrate, where Q is selected from the group consisting essentially of $As_{1-y}P_y$ and 0< x<1, $0\le y\le 1$.
 - 2. The device of claim 1 where y=1.
 - 3. The device of claim 2 where x=0.67.
 - 4. The device of claim 2 where x=0.57.
 - 5. The device of claim 1 where x=0.24.
- 6. The device of claim 2 wherein the layer is formed on the substrate and the substrate portion on which the layer is formed consists essentially of InP.
- 7. The device of claim 6 wherein the layer is doped to have a first conductivity polarization.
- 8. The device of claim 7 wherein another layer of $In_{1-x}T\ell_xP$ having a second conductivity polarization is formed on the layer having the first polarization to form a p-n homojunction.
 - 9. The device of claim 1 where y=0.
 - 10. The device of claim 9 where x=0.15.
- 11. The device of claim 10 wherein the layer is formed on the substrate and the substrate portion on which the layer is formed consists essentially of InAs.

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- 12. The device of claim 1 wherein the layer is doped to have a first conductivity polarization, and a second layer having substantially the same compound as the layer having the first conductivity polarization contacting the first conductivity polarization layer to form a p-n homojunction, the second layer being doped to have a second conductivity polarization.
- 13. The device of claim 1 wherein the layer is formed on the substrate and the substrate portion on which the layer is formed consists essentially of InQ, the layer and substrate having substantially the same lattice constants.
 - 14. The device of claim 13 where y=1.
 - 15. The device of claim 13 where y=0.
 - 16. The device of claim 13 where $0 \le y \le 1$.
 - 17. The device of claim 1 where $0 \le y \le 1$.
- 18. The device of claim 1 wherein the substrate includes a second layer of ${\rm In_{1-2}T\ell_2Q}$, where z is less than x, the second layer being positioned above the ${\rm In_{1-x}T\ell_xQ}$ layer so certain optical radiation wavelengths incident on the second layer pass through the second layer and are absorbed by the ${\rm In_{1-x}T\ell_xQ}$ layer and other optical radiation wavelengths incident on the second layer are absorbed thereby.
- 19. The device of claim 18 where y = 1, x = 0.67, z = 0.57.
 - 20. The device of claim 1 where y = 1, x = 0.24.

- 21. The device of claim 1 further including an ohmic contact on the layer.
- 22. $In_{1-x}T\ell_xQ$, where Q is selected from the group consisting essentially of $As_{1-y}P_y$ and 0< x<1, $0\le y\le 1$.
 - 23. The composition of claim 22 where y=0.
 - 24. The composition of claim 22 where y=1.
 - 25. The composition of claim 22 where 0<y<1.

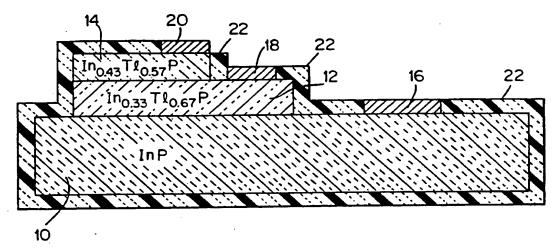


FIG. 1

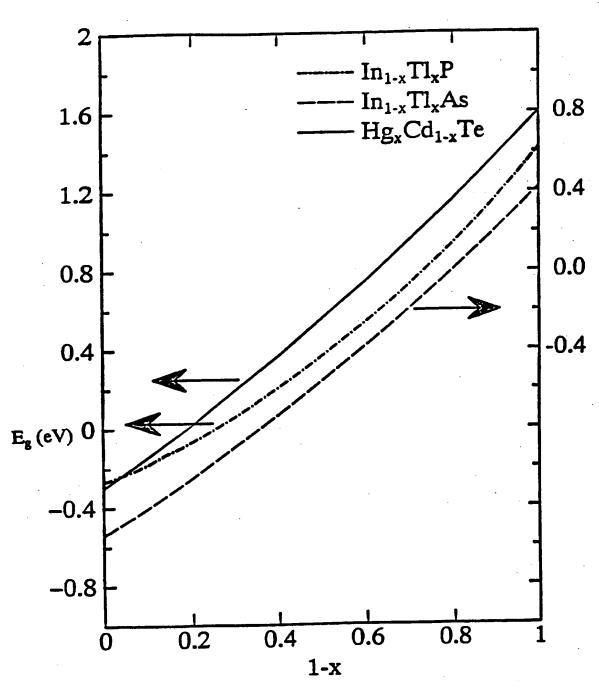
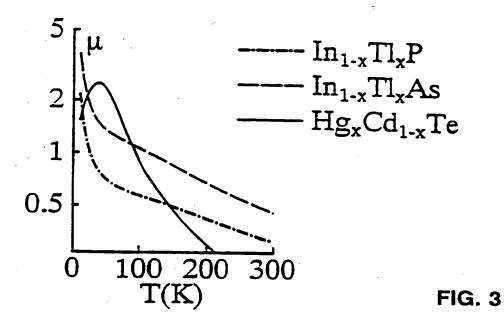


FIGURE 2



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